

LIST OF PROJECT ASSIGNMENTS FOR SECOND SEMESTER 2009-2010

- 1 **TORRENT RESEARCH CENTRE, AHMEDABAD**
 - 1 FORMULATION DEVELOPMENT AND EVALUATION OF LIPOSOMAL DRUG DELIVERY SYSTEM.
- 2 **TATA CHEMICALS LTD., BARRALA (UP)**
 - 2 SIMULATION AND SENSITIVITY ANALYSIS OF CO₂ REMOVAL SECTION AND DESIGN ANALYSIS OF A STEAM EJECTOR FOR MODIFYING PROCESS
 - 3 SIMULATION AND SENSITIVITY ANALYSIS OF MEDIUM PRESSURE (MP) ABSORBER (C-01) IN AN UREA PLANT
- 3 **ARDC DESIGN COMPLEX - HAL BANGALORE**
 - 4 DESIGN OF WHEEL CORRECTION LINKAGE MECHANISM IN MAIN LANDING GEAR OF NAVAL AIRCRAFT
 - 5 STRESS AND FATIGUE ANALYSIS OF ARRESTOR-HOOK PLATFORM IN A NAVAL AIRCRAFT
 - 6 DESIGN OF LANDING GEAR FOR A TRAINER AIRCRAFT
 - 7 DESIGN OF GROUND TEST RIG FOR A TRAINER AIRCRAFT
 - 8 IMPLEMENTATION OF VIRTUAL COCKPIT AND OTHER FEATURES FOR DATA DEBRIEF TOOL
- 4 **ARM, BANGALORE**
 - 9 MEMORY COMPILER CHARACTERIZATION AND DEVELOPMENT
 - 10 VERIFICATION OF V7MP ARCHITECTURE APPLICATION ARM PROCESSOR WITH VIRTUALIZATION EXTENSION
- 5 **ADITYA BIRLA MINACS / MADURA GARMENTS, BANGALORE**
 - 11 BUDGETING AND PLANNING SOLUTIONS USING ORACLE HYPERION PLANNING
 - 12 PRODUCT LIFE CYCLE MANAGEMENT
- 6 **AIRBUS ENGINEERING CENTER INDIA, BANGALORE**
 - 13 PORTING AMBE ON XTREEMOS AS A GRIDBASED TOOL & FINDING SIMILAR IMAGES BASED UPON CLUSTERING AND DEVELOPING A GUI FRONTEND
 - 14 BUILDING A PLATFORM ABLE TO SUPPORT A NETWORK FOR GLOBAL COLLABORATION AND KNOWLEDGE SHARING
- 7 **ALTAIR ENGINEERING INDIA PVT. LTD., BANGALORE**
 - 15 DEFECT ANALYTICS AND DEVELOPMENT OF VISUALIZATION WIDGETS
 - 16 USER INTERFACE DEVELOPMENT FOR CFD USER PROFILE FOR HYPERMESH 11.0
 - 17 DEVELOPMENT OF GEAR BUILDER UTILITY FOR HYPER WORKS - MOTION VIEW
 - 18 UNDERSTANDING ALTAIR POST-PROCESSOR TOOL HYPERVIEW AND ITS COMMAND LAYER TCL API
 - 19 UTILITIES FOR ENGINE TEST DATA MANAGEMENT SOFTWARE
- 8 **AMAZON DEVELOPMENT CENTRE, BANGALORE**
 - 20 INTEGRATION OF MEMCACHED WITH DATABASE DRIVEN SYSTEM AND SETTINGUP OF REPORTING FRAMEWORK FOR CONTENT VALIDATING SYSTEM
 - 21 SETUP AN AGGREGATION AND REPORTING PIPELINE FOR AMAZON AD-NETWORKS
 - 22 STATISTICAL ANALYSIS FOR TUNING AND DEFINING HEURISTICS FOR ONLINE ADVERTISING PLATFORMS
- 9 **ANALOG DEVICES, BANGALORE**
 - 23 DESIGN AND ANALYSIS OF DIGITAL TO ANALOG CONVERTERS
 - 24 DESIGN VERIFICATION OF SOC PERIPHERALS AND IMPLEMENTATION OF QUICK BOOT
 - 25 VERIFICATION OF PERIPHERALS OF SOC
 - 26 DESIGN OF DIGITAL TEMPERATURE SENSOR
- 10 **APEX DECISIONS, BANGALORE**
 - 27 CALIBRATION AND ALLOCATION - RESEARCH & DEVELOPMENT
 - 28 FRAMEWORK DEVELOPMENT
- 11 **BOSCH LTD., BANGALORE**
 - 29 (A) STANDARDISATION OF CLASS STRATEGY AND VALIDATION ON THE LABCAR 2
STANDARDISATION OF ERROR REACTIONS
(B) CREATION OF A DATABASE OF REFERENCE DOCUMENTS FOR DFC ERROR PATHS,
FID REACTIONS AND DSM STANDARDIZATION ACROSS DIFFERENT PLATFORMS